

Fig. 1

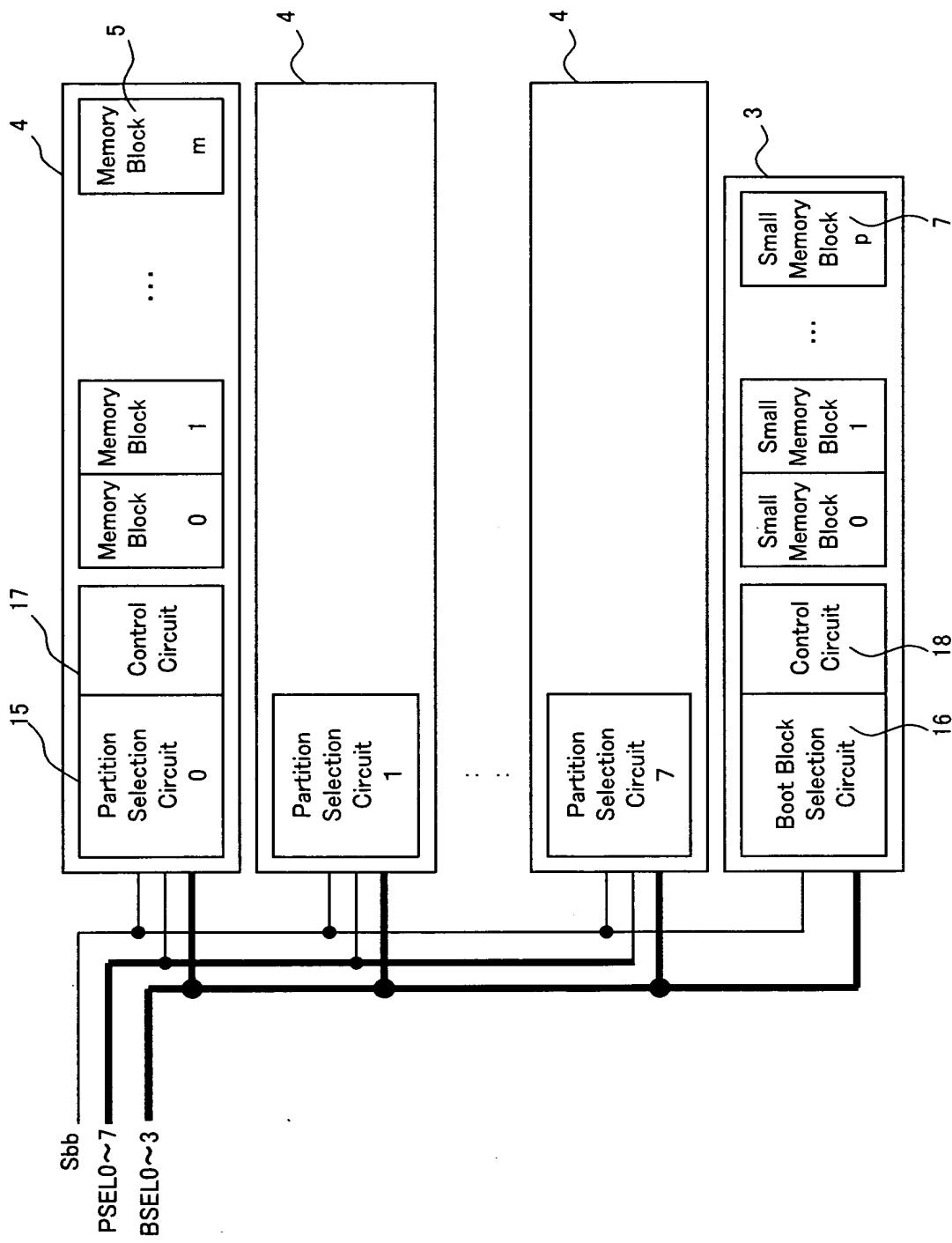


Fig. 2

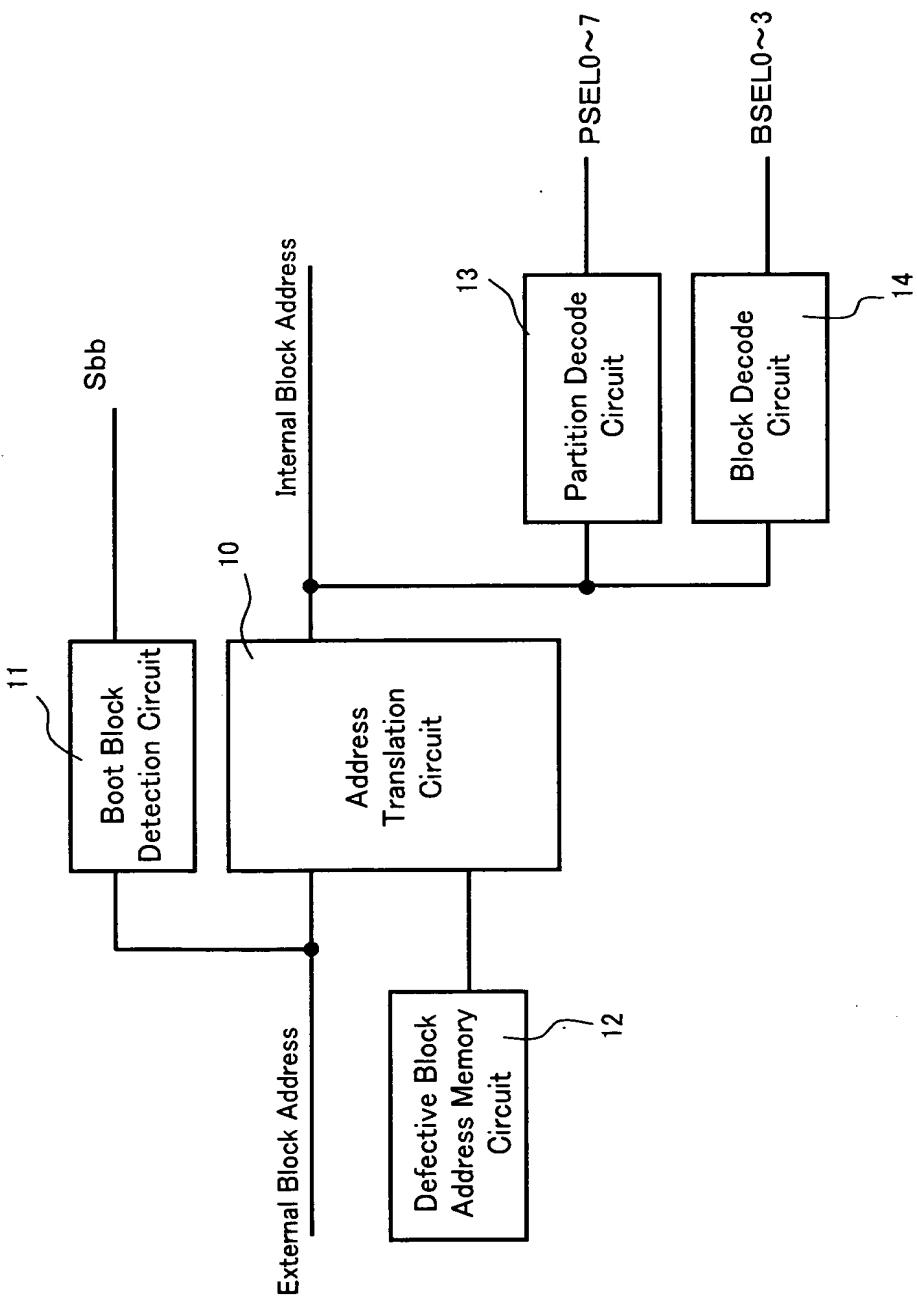


Fig. 3

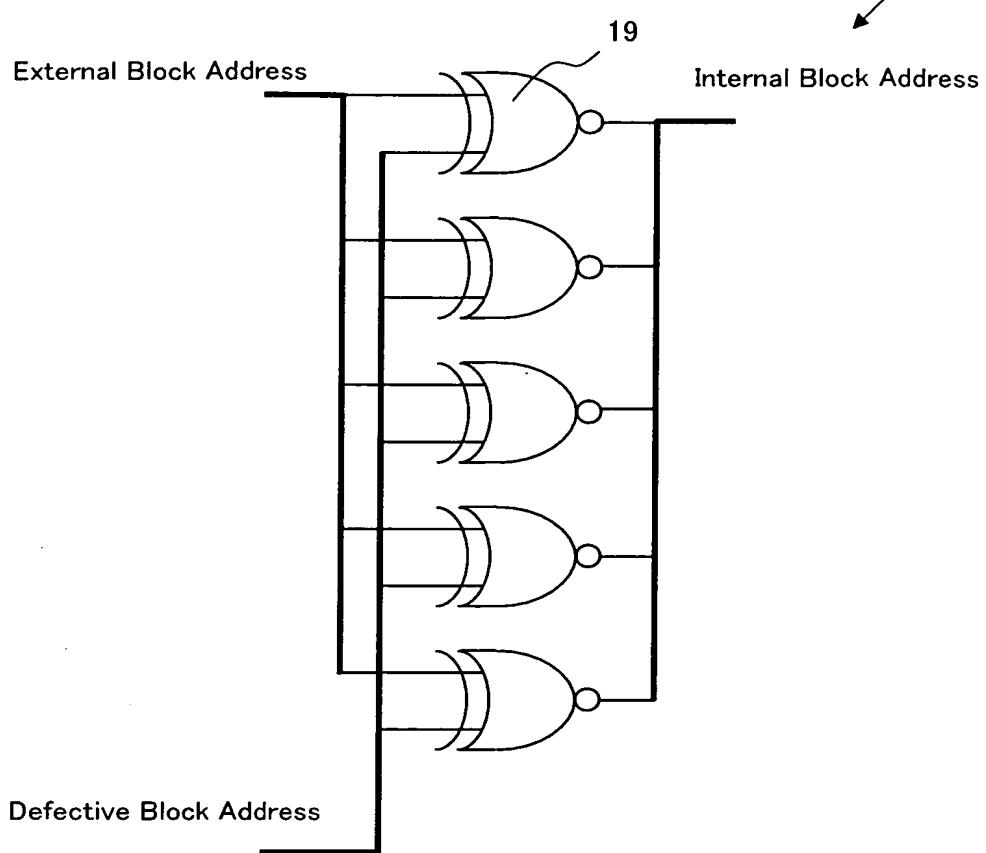


Fig. 4

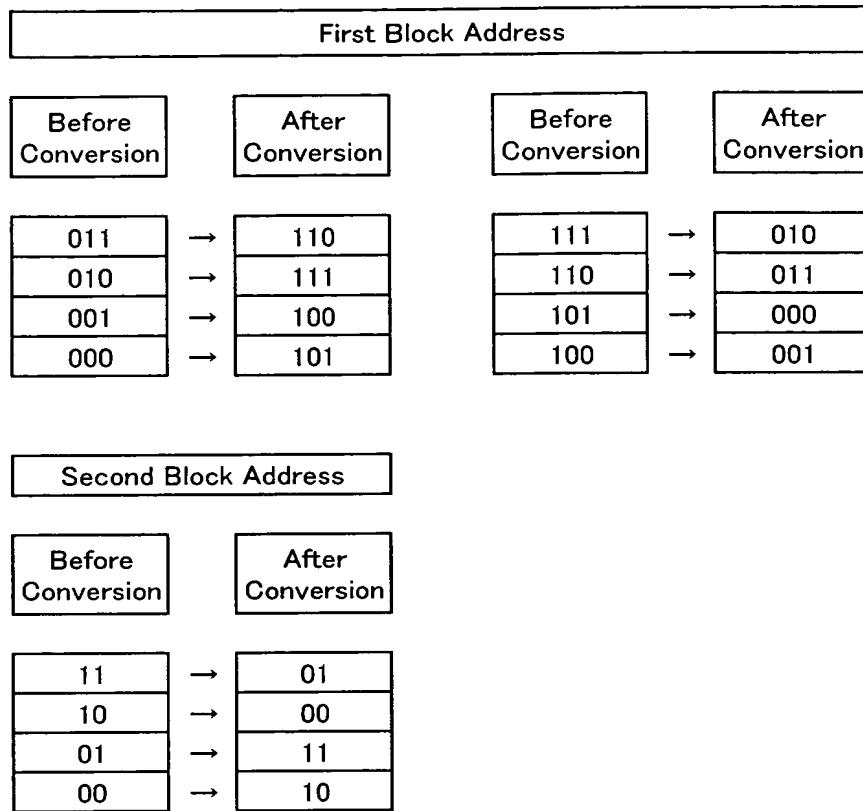
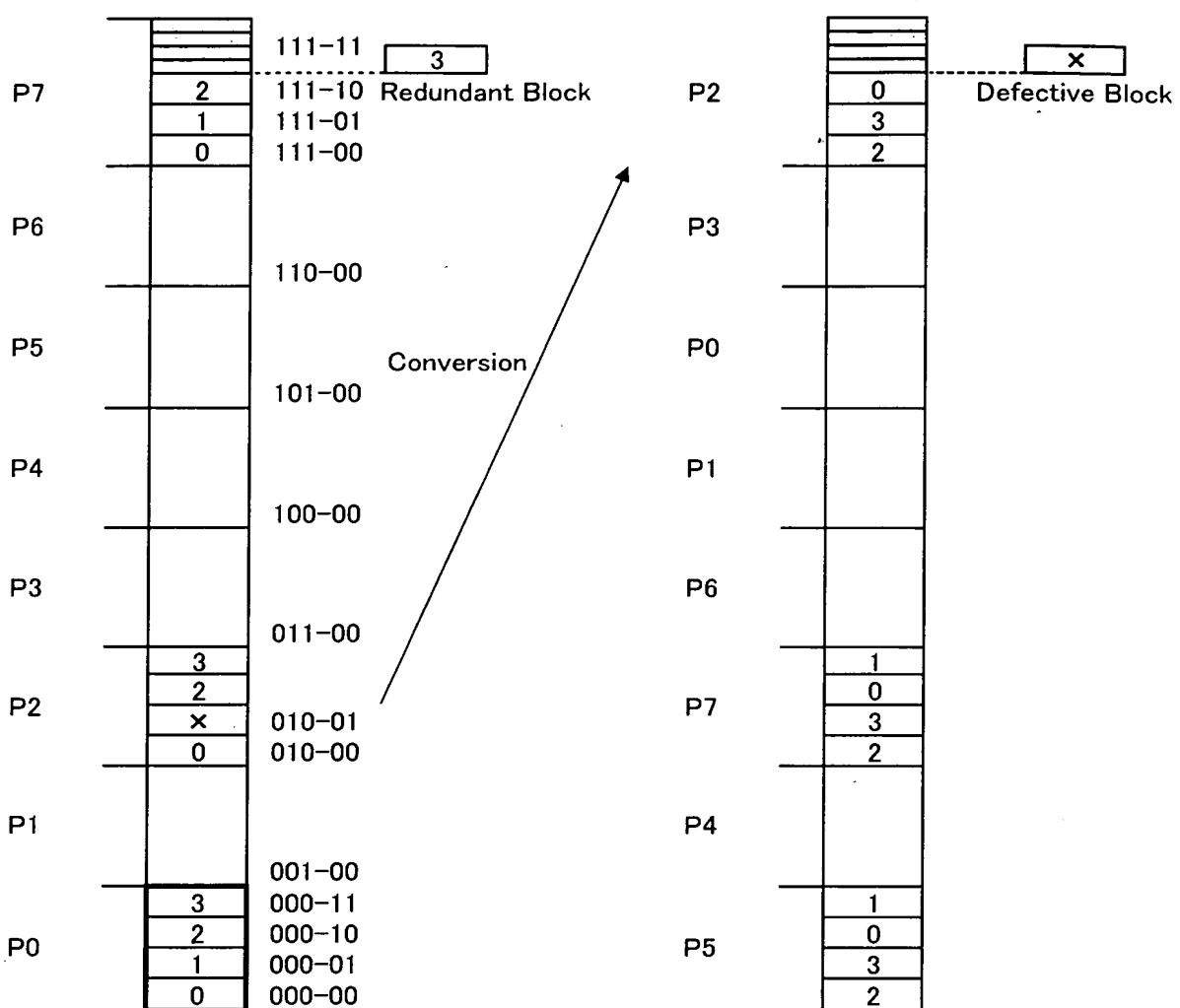


Fig. 5



Physical Block Address Map

Logical Block Address Map

Fig. 6

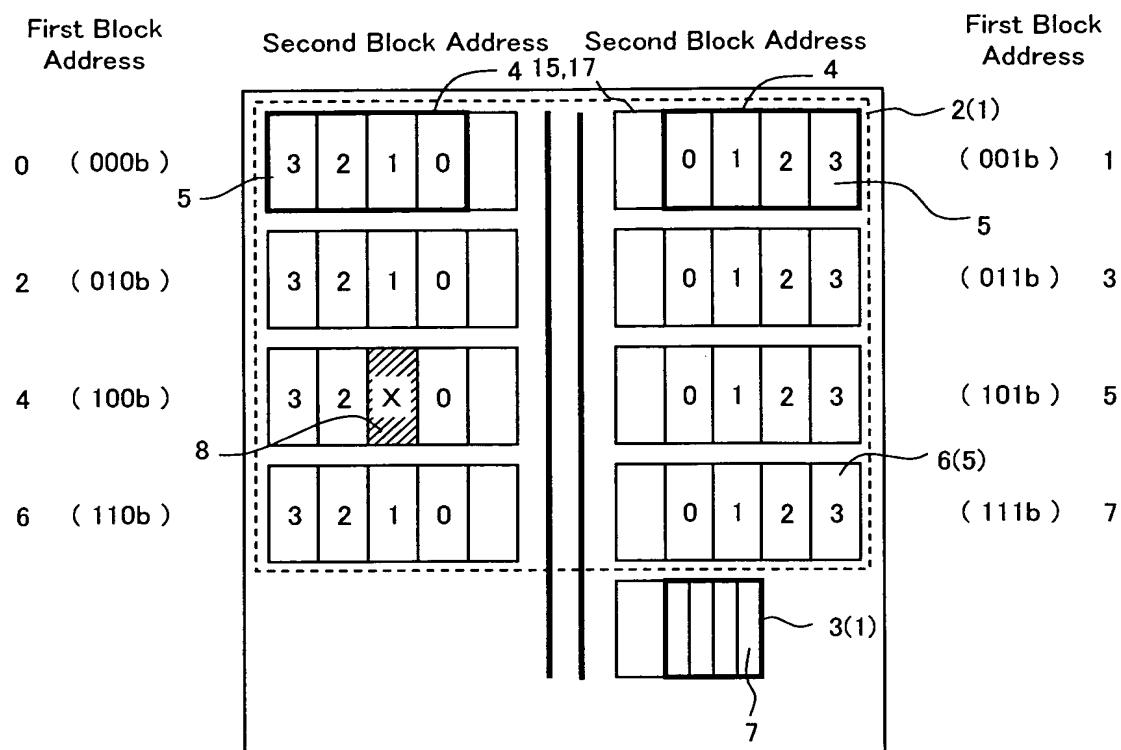


Fig. 7

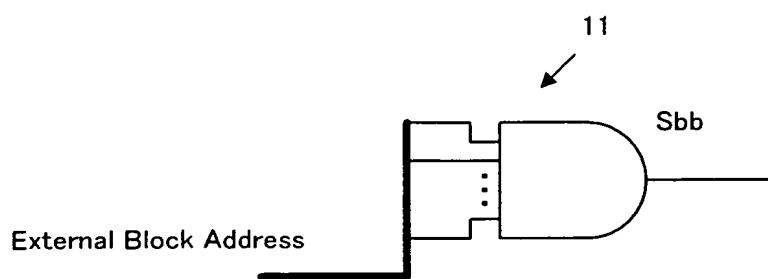


Fig. 8